

PATENT
ATTY DOCKET NO.: RLC-74**Listing of the Claims:**

1-7 (Canceled)

- 1 8. (Currently amended) A method for transferring large amounts of complex data
2 between a data link module and a host across a bit level network, said method comprising
3 the steps of:
- 4 (a) configuring a channel set to said data link module;
5 (b) configuring a frame address to said data link module;
6 (c) sending a multi-bit message from said host to said data link module, said
7 multi-bit message including a message command segment on a first channel of said
8 channel set at said data link module frame address and a message data segment on a
9 second channel of said channel set at said data link module frame address, said message
10 command segment including a register operand and at least either of a read request or a
11 write request;
- 12 (d) accessing a register in said data link module specified in said register operand
13 as a specified register and reading a value from said specified register as a read value in
14 response to said read request or writing said message data segment to said specified
15 register in response to said write request;
- 16 (e) sending, on a first channel of said channel set at said data link module frame
17 address, a reply from said data link module to said host said reply including a reply
18 command segment equal to said message command segment [on a first channel of said
19 channel set at said data link module frame address] and on a second channel of said
20 channel set at said data link module frame address, a reply data containing said read value
21 in response to said read request or being equal to said message command segment in
22 response to said write request [on a second channel of said channel set at said data link
23 module frame address].

9-13 (Canceled)

PATENT
USSN: 09/686,178

1 14. (Currently amended) A method for transferring large amounts of complex data
2 between a data link module and a host across a bit level network , said method
3 comprising the steps of:

4 (a) configuring a channel set having at least two bit level time division
5 multiplexed channels to said data link module;

6 (b) configuring a frame address to said data link module;

7 (c) sending a message from said host to said data link module, said message
8 including a message command segment on a first channel of said channel set at said data
9 link module frame address and a message data segment on at least one other channel of
10 said channel set at said data link module frame address, said message command segment
11 including a register operand and at least either of a read request or a write request;

12 (d) accessing a register in said data link module specified in said register operand
13 as a specified register and reading a value from said specified register as a read value in
14 response to said read request or writing said message data segment to said specified
15 register in response to said write request;

16 [(f)] (e) sending a reply from said data link module to said host, said reply
17 including a reply command segment equal to said message command segment on a first
18 channel of said channel set at said data link module frame address and a reply data
19 segment containing said read value in response to said read request or being equal to said
20 message command segment in response to said write request on at least one other channel
21 of said channel set at said data link module frame address.

15-18 (Canceled)